Steffan Owen

Personal Statement

I am a dedicated and enthusiastic learner who is not afraid to ask questions. I love solving problems and have the skills to do so on my own, or as part of a team. I keep myself organised and plan ahead. My interests include: climbing, hiking, video games, TV and film, and keeping up to date with developments in the tech industry. I enjoy learning about anything and everything. In October and November 2023 I travelled to Nepal and the Philippines, ending in New Zealand.

Education

First Class Masters of Engineering (with Honours)   **University of Warwick, 2018 - 2023**
Integrated 4 year Masters in Computer Systems Engineering (with Intercalated Year)
A*A*AA at A-level. 11 GCSEs.   **Aquinas College, Marple Hall Secondary, 2011 - 2018**
A-levels: Maths, Computer Science, Further Maths, Physics. GCSE's include Maths and English.

Experience

FPGA Based Smart NICs Intern, Intel UK   **July 2020 - August 2021**
- Work with Formal Verification technologies (SystemVerilog Assertions, JasperGold), utilising: Formal Property Verification, Cone of Influence evaluation, X-Propagation
- Developed a memory model for simulation test-bench environments in Questasim
- New simulation schema involving: OOP, SystemVerilog interfaces, build script config
- RJ45 board evaluation: FPGA Hardware design, high speed electronics, device config

Summer Play Worker, Kickstart UK   **August 2023**
- Collaborated to organise and deliver sports and crafts activities for children in the UK
- Ensured the children's safety and well being while engaging with them pro-actively

Student and Whānau Support Advisor, Te Kura   **November 2023 - February 2024**
- Delivered pastoral and technical support to students and whānau over email and phone
- Communicated ongoing issues with the team and problem solved together
- Developed professional relationships while adapting to, and living in, a new country

Skills

Programming Languages ++: C, C++, C#, Python, LaTeX , MATLAB, Java, BASH, JavaScript, HTML, CSS, SystemVerilog/Verilog, Git, Linux development

Technical: Digital and analogue circuit design, wireless network design, Machine Learning and AI development, micro-sensor design, IoT devices, computer building

Soft: Good communication , effective planner, completely IT competent, disciplined, excellent problem solving, a friendly face, team player, and hard worker

Projects

Wearable VOC Monitor for Biomedical Applications (3rd Year Project): Development of an electronic nose with a neural network and a web interface to detect urine on the fly after being trained on a large data set. This utilised Keras, Python, and TensorFlow.

Rapid Additive Manufacture of Metals (4th Year Group Project): The group continued work on a WAAM cartesian rig to 3D print metal. The group investigated the effects of thermal management of the weld pool on the weld material's mechanical and macroscopic properties. Extensive work was done to control motors and electronics.